

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. BU998050DIV1		SERIAL NO. 09/805,200	
				APPLICANT(S) William P. Moore, et al.			
				FILING DATE March 14, 2001		GROUP 2181	

PTO
DEC 05 2003
TRADEMARK OFFICE

U.S. PATENT DOCUMENTS

☐ **IDC-US Ref -2**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SG	2003/0070013 A1	04/10/03	Hansson			
SG	6,397,710 B1 <i>6,397,170</i>	05/28/02	Dean, et al.			

RECEIVED
DEC 08 2003
Technology Center 2100

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

IDC-Other Ref -1

SG			Ching-Long Su, et al., "Low Power Architecture Design and Compilation Techniques for High-Performance Processors", 1994, Pages 489-498.

EXAMINER <i>[Signature]</i>	DATE CONSIDERED 4/6/04
---------------------------------------	---

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.